

FIG. 1

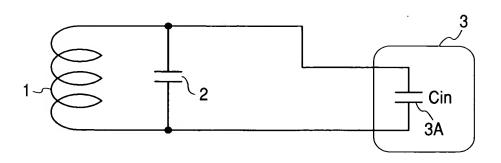
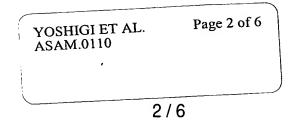
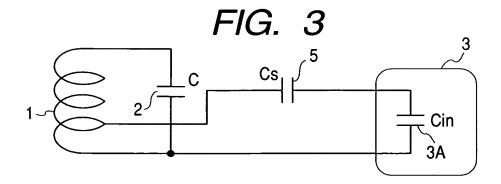
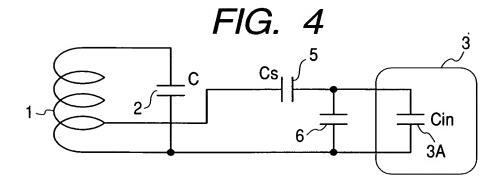


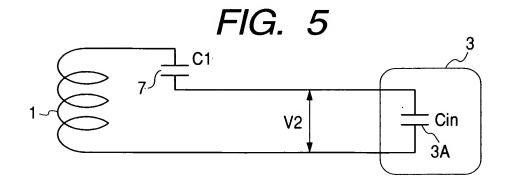
FIG. 2

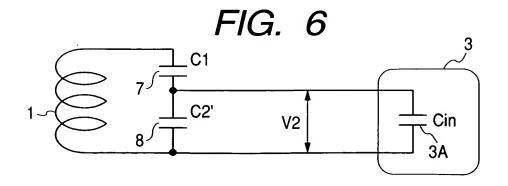
IC CHIP INPUT CAPACITANCE Cin(nE) (DEVIATION)		RESONANT FREQUENCY		COMMENTS
Cin(pF) 26	(DEVIATION) (+30%)	fres(MHz) 12.38	(DEVIATION) (-8.7%)	
20	(0)	13.56	(0)	$L = 4.59 (\mu H)$
14	(-30%)	15.16	(+11.8%)	C2' = 10 (pF)











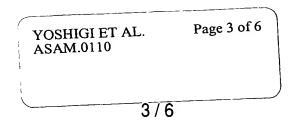
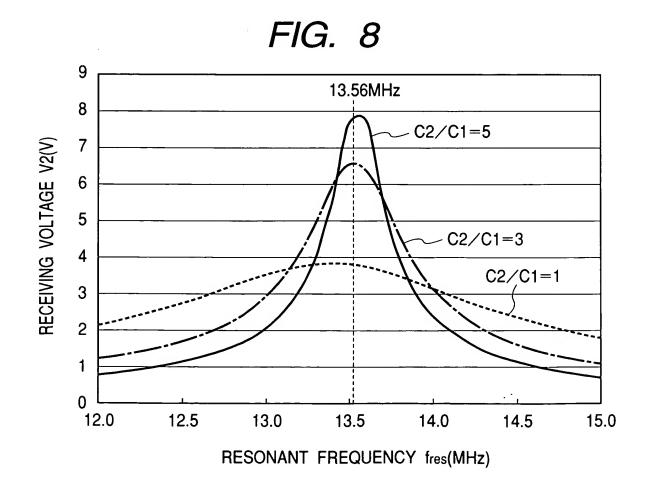


FIG. 7

IC CHIP INPUT CAPACITANCE		RESONANT FREQUENCY		COMMENTS	
Cin(pF)	(DEVIATION)	fres(MHz)	(DEVIATION)	OOMMENTO	
26	(+30%)	13.46	(-0.8%)	1 00(441)	
20	(0)	13.58	(0)	L = 9.6 (μ H) C1 = 18 (pF)	
14	(-30%)	13.70	(+1.0%)	C2' = 50 (pF)	



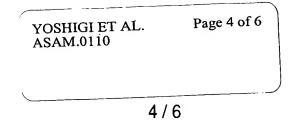
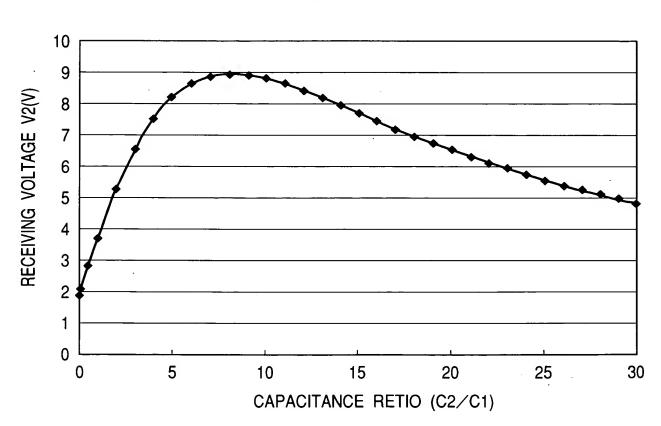


FIG. 9



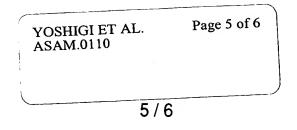
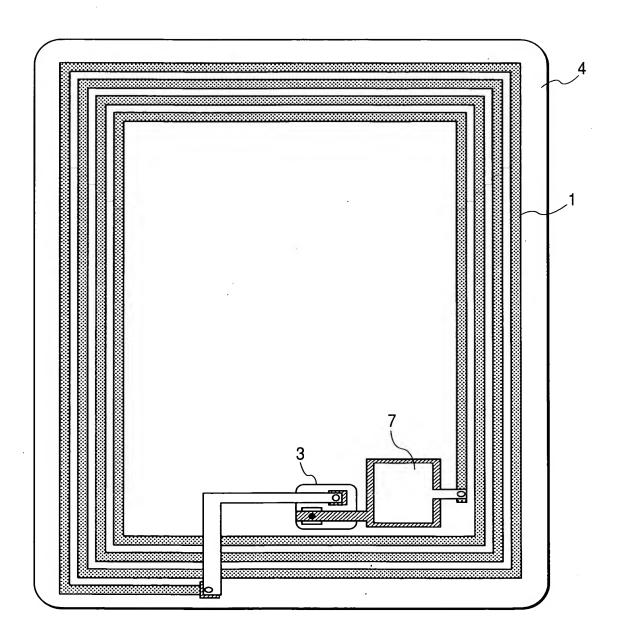


FIG. 10



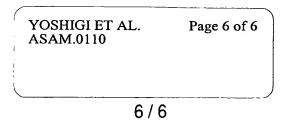


FIG. 11

